



## Fifth Haifa Verification Conference – Call for Papers

### Keynote Speakers

David Harel  
Mark Harman

October 19 – 22, 2009

Haifa Verification Conference 2009

### General Chair

Avi Ziv

<http://www.haifa.il.ibm.com/conferences/hvc2009/>

### Program Chairs

Kedar Namjoshi  
Andreas Zeller

HVC 2009 is the fifth in the series of annual conferences dedicated to advancing the verification and testing of both hardware and software. The conference provides a forum for academia, industry, and the research and development community to share their work, exchange ideas, and discuss the challenges and future directions of verification for hardware, software, and hybrid systems.

### Program committee

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Eyal Bin  
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Hana Chockler  
Myra Cohen  
Christoph Csallner  
Kerstin Eder  
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Andreas Podelski  
Gil Shurek  
Scott Stoller  
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Tao Xie  
Eran Yahav  
Karen Yorav

Verification methods are generally divided into two paradigms – formal verification and dynamic verification (testing). Within each paradigm, different techniques are used for hardware and software systems. HVC is the only conference that brings together researchers from all verification fields, thereby encouraging the migration of methods and ideas between domains. We encourage submission of works that exemplify the successful integration of ideas from different domains of expertise.

Accepted papers will be presented at the conference and published in post-conference proceedings in Springer's Lecture Notes in Computer Science series (LNCS). The top 10%-20% papers will be invited to submit journal versions of their work to a special edited issue of STTT (<http://sttt.cs.uni-dortmund.de/>).

Topics of interest to the conference include, but are not limited to:

- |   |                                   |
|---|-----------------------------------|
| Simulation-based verification               | Formal verification methods       |
| High-level stimuli generation               | Model checking                    |
| Equivalence checking                        | SAT-based verification algorithms |
| Classification of hardware bugs             | Static analysis                   |
| Design for verifiability                    | Concurrency testing               |
| Hardware/software co-verification           | Debugging                         |
| Emulation and acceleration                  | Defect prevention                 |
| CSP applications in functional verification | Test-driven development           |
| Hybrid verification methods                 | Model-based testing               |
| Verification coverage                       | Developer testing                 |
| Formal Specification Languages              | Review and inspection             |
| Semi-formal verification                    | Pair testing and first testing    |
| Test automation frameworks                  | Automatic test generation         |

Authors are invited to submit papers describing original, unpublished work. Papers are to be submitted electronically through the conference web page, in LNCS format (<http://www.springer.com/east/home/computer/lncs>). The page limit is fifteen (15) for regular papers and five (5) for tool papers. Simultaneous submission to other conferences is not allowed.

### Important dates:

Full papers submission deadline	<b>July 13, 2009</b>
Notification of acceptance:	<b>August 28, 2009</b>
Conference gathering:	<b>October 19-22, 2009</b>
Final version of accepted papers:	<b>November 20, 2009</b>

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