HVC 2007 is the third annual conference dedicated to advancing the verification and testing of both hardware and software. The conference provides a forum for academia, industry, and the research and development community to share their work, exchange ideas, and discuss the challenges and future directions in the field.

Verification methods generally divide into two paradigms – formal verification and dynamic verification (testing). Within each paradigm, different techniques are used for hardware and software systems. Yet all are aimed at the same goal of ensuring the correct functionality of a complicated system. HVC is the only conference that brings together researchers from all verification fields, thereby encouraging the migration of methods and ideas between domains. To this end, we are announcing a special interest in hybrid methods for this year’s conference. We encourage the submission of works that exemplify the successful integration of ideas from different domains of expertise.

Accepted papers will be presented at the conference and published in a post-conference proceedings in Springer's Lecture Notes in Computer Science series (LNCS). The top 10%-20% papers will be invited to submit journal versions of their work to a special edited issue of STTT (http://sttt.cs.uni-dortmund.de/).

Topics of interest include, but are not limited to:

- Simulation-based verification
- High-level stimuli generation
- Equivalence checking
- Classification of hardware bugs
- Design for verifiability
- Hardware/software co-verification
- Emulation and acceleration
- CSP applications in functional verification
- Hybrid verification methods
- Verification coverage
- Formal Specification Languages
- Semi-formal verification
- Test automation frameworks
- Formal verification methods
- Model checking
- SAT-based verification algorithms
- Static analysis
- Concurrency testing
- Debugging
- Defect prevention
- Test-driven development
- Model-based testing
- Developer testing
- Review and inspection
- Pair testing and first testing
- Automatic test generation

Authors are invited to submit papers describing original, unpublished work. Papers are to be submitted electronically through the conference web page, in LNCS format (http://www.springer.com/east/home/computer/lncs). The page limit is fifteen (15) for regular papers and five (5) for tool papers. Simultaneous submission to other conferences is strictly forbidden.

**Important dates:**

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