Haifa Verification Conference – Call for Papers & Tools

October 23 – 26, 2006

Organized by the IBM Research Lab in Haifa

http://www.haifa.il.ibm.com/Workshops/verification2006/

The second annual Haifa Verification Conference provides a forum for academia, industry, and the research and development community to share their work, exchange ideas, and discuss challenges and future research directions in the areas of hardware verification and software testing. The conference includes three tracks: hardware verification technologies and methodologies, software testing, and tools for hardware verification and software testing. In addition, the conference includes a half-day tutorial on PSL. Topics of interest to the conference include, but are not limited to:

Hardware Verification

- Simulation-based verification
- High-level stimuli generation
- Verification coverage
- Simulation checking
- Formal methods and their applications
- Model checking
- Verification using SAT
- Equivalence checking
- PSL methodologies and experience
- Classification of hardware bugs
- Design for verifiability
- Hardware/software co-verification
- Hybrid verification methods
- Emulation and acceleration
- CSP applications in functional verification

Software Testing

- Using static analysis in testing
- Concurrency testing
- Debugging
- Formal verification of SW
- Defect prevention
- Test-driven development
- Model-based testing
- Developer testing
- Review and inspection
- Pair testing and first testing
- Automatic test generation
- Test automation frameworks

The organizing committee invites authors to submit papers describing original and unpublished work to all three tracks of the conference. Submitted papers should not exceed fifteen pages in LNCS (Springer Lecture Notes in Computer Science) format. (For details, see http://www.springer.com/east/home/computer/lncs?SGWID=5-164-7-72376-0). The submission procedure will be published on the conference web page (http://www.haifa.il.ibm.com/Workshops/verification2006/call_for_papers.html). An early email to Eyal Bin (bin@il.ibm.com) with your intention to submit a paper would be greatly appreciated. Accepted papers will be presented at the conference and published in a post-conference Springer Lecture Notes in Computer Science book (LNCS).

The organizing committee also invites the submission of hardware verification and software testing tools to the tools track. We welcome tools from both academia and industry. Tool submissions should include an extended abstract describing the tool, not exceeding five pages in LNCS format. Accepted tools will be presented at the conference (short presentation) and, if desired, demo'ed during the conference. Note that authors of regular accepted papers on tools are also invited to demo their tools during the conference.

Important dates:

Paper and tools submission deadline:	July 6, 2006
Notification of paper acceptance:	September 1, 2006
Conference gathering and presentations:	October 23 – 26, 2006
Final version of accepted papers:	November 17, 2006

Program committee

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