



IBM Austin Conference on Energy-Efficient Design 2007

SECOND CALL FOR POSTERS

**March 5, 2007
Austin, Texas**

Sponsored by the IBM Austin Research Laboratory

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The IBM Austin Research Laboratory is pleased to request posters to be presented at the fifth annual Austin Conference on Energy-Efficient Design (ACEED 2007). This conference provides a forum for the exchange ideas on power management, low-power techniques and methodologies, and promotes research on critical areas relating to energy-efficient computing.

Topics of interest include but are not limited to:

Energy-efficient VLSI

- Microprocessors, microarchitectures and circuits
- Custom accelerators
- CMOS devices and technology

Tools for energy-efficient design

- Logic synthesis
- Verification
- Physical design
- Manufacturability

Impact of variability on power consumption

- Post-silicon characterization and yield improvements
- Opportunities for power management
- Variability-tolerant circuits and microarchitectures

Systems and datacenters

- Measurements of commercial or prototype systems
- Power/performance benchmarking and standard efforts
- Power-aware compilers, applications, operating systems and hypervisors
- Storage, network and embedded systems design
- Monitoring and management of temperature, power and energy
- Power, voltage, current and temperature sensors
- Power conversion and distribution
- Systems packaging
- Datacenter and systems cooling and planning

Submission format

Overview of the poster – 1 page (maximum). Including a rough sketch of the poster and/or a URL related to the poster is strongly recommended. Please use ASCII text or Adobe PDF format. Submissions should be sent to aceed@us.ibm.com.

For more information visit the ACEED web site: <http://www.research.ibm.com/aceed>

IMPORTANT DATES

February 2, 2007:
Extended deadline
for posters

February 7, 2007:
Author notification

March 5, 2007:
ACEED conference